## **DECLARATION**

As a below named inventor, we hereby declare that:

My residence, post office address and citizenship are as stated below next to my name; that

We verily believe we are the original and first inventor(s) of the invention entitled: TRENCH BURIED BIT LINE MEMORY DEVICES AND METHODS THEREOF, described and claimed

X in the attached specification;	
in the specification filed	, as U.S. Application Serial No.
, and as amended _	·

We hereby authorize the attorney(s) and/or agent(s) appointed herein to indicate above whether the invention is described and claimed in an attached specification and to provide the Filing Date and Serial No. of the corresponding U.S. Application, if previously filed.

We hereby state that we have reviewed and understand the contents of the above identified specification, including the claims as filed and as amended by any amendment referred to above.

We acknowledge the duty to disclose to the Patent and Trademark Office all information known to us to be material to patentability as defined in Title 37, Code of Federal Regulations, §1.56(a).

We further declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under §1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Inventor's signature	uan C Tran	
Date: October	22,2002	
	Sandy Court , Idaho 83642	
Citizenship: USA		
Post Office Address: c/o	Micron Technology, Inc. 8000 S. Federal Way Boise, ID 83706-9632	
Full name of second Inventor: Mark Durcan Inventor's signature		
Date: 10/2H 02	_	
Residence: 4850 Boise River Lane Boise, Idaho 83716		
Citizenship: USA		
Post Office Address: c/o	Micron Technology, Inc. 8000 S. Federal Way Boise, ID 83706-9632	

Full name of sole or first Inventor: Luan C. Tran

Full name of third Inventor: Howard C. Kirsch

Inventor's signature Monitoral Kirch

Date: 10/24/02

Residence:

479 W. Boggs View Drive

Eagle, Idaho 83616

Citizenship: USA

Post Office Address: c/o Micron Technology, Inc.

8000 S. Federal Way Boise, ID 83706-9632

## **POWER OF ATTORNEY**

Applicant: Luan C. Tran; Mark Durcan; Howard C. Kirsch
Application No.: Filed:
Entitled: TRENCH BURIED BIT LINE MEMORY DEVICES AND METHODS THEREOF
CED TIELCA TE I DIDED 27 OPD 2 724
CERTIFICATE UNDER 37 CFR 3.73(b)
Micron Technology, Inc., a corporation of the State of Delaware, with a place of business at 8000 S. Federal Way, Boise, ID 83706-9632 certifies that it is the assignee of the entire right, title and interest in the patent application identified above by virtue of either:
A. [X] An assignment from the inventor(s) of the patent application identified above, a copy of which is attached.
OR .
B. [ ] A chain of title from the inventor(s), of the patent application identified above, to the current assignee as shown below:
1. From:To:
The document was recorded in the Patent and Trademark Office at
Reel, or for which a copy thereof is attached.
2. From:To:To:The document was recorded in the Patent and Trademark Office at
Reel, or for which a copy thereof is attached.
3 From: To.
3. From:To:To:The document was recorded in the Patent and Trademark Office at
Reel, or for which a copy thereof is attached.
, or for which a copy thereor is attached.
[ ] Additional documents in the chain of title are listed on a supplemental sheet.
Copies of assignments or other documents in the chain of title are attached.
The undersigned has reviewed all the documents in the chain of title of the patent application identified above and, to the best of undersigned's knowledge and belief, title is in the assignee identified above.
The undersigned (whose title is supplied below) is empowered to sign this certificate on behalf of the assignee.

Micron Technology, Inc. hereby appoints the following attorney(s) and/or agent(s) to prosecute this application and to transact all business in the Patent and Trademark Office connected therewith:

)

Richard A. Killworth James F. Gottman Timothy W. Hagan James E. Beyer Susan M. Luna Patricia L. Prior William A. Jividen Thomas E. Lees John D. Reed Brian L. Smiler Julie G. Cope	Reg. No. 26,397 Reg. No. 27,262 Reg. No. 29,001 Reg. No. 39,564 Reg. No. 38,769 Reg. No. 33,758 Reg. No. 42,695 Reg. No. 46,867 Reg. No. 46,867 Reg. No. 46,506 Reg. No. 46,458 Reg. No. 48,624
	,

Address all telephone calls to (937) 223-2050. Address all correspondence to: KILLWORTH, GOTTMAN, HAGAN & SCHAEFF, L.L.P., One Dayton Centre, One South Main Street, Suite 500, Dayton, Ohio 45402-2023.

Micron Technology, Inc. hereby declares that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

Date: 11-25-2002	-
Name: Michael L. Lynch	
Title: Chief Patent Counsel	
Signature:	